

AMENDMENTS TO THE SPECIFICATION:

Replace the paragraph beginning at page 4, line 11, with the following amended paragraph:

However, in the second transmission data DATA_2B, DATA_1B, DATA_0B, the data reading timing ~~[[401]] 402~~ precedes the transition point in the transmission data DATA_2B whereas it falls after the transition point in the transmission data DATA_1B and DATA_0B. Consequently, the databits of the reception data DATA_2C, DATA_1C, and DATA_0C become "111", as compared to the databit ~~[["010"]]~~ "011" of the transmitting end. As a result, the receiver receives an incorrect data and consequently performs an incorrect operation.

Replace the paragraph beginning at page 11, line 11, with the following amended paragraph:

The transmitter 10a includes transmission flip-flops 11a, ~~[[12a]] 11b~~, and 11c, which carry out a synchronous operation according to the transmit clock CLOCK_A, and control flip-flops 12a and 12b. Transmission data DATA_2, DATA_1, and DATA_0 are input into the transmission flip-flops 11a, ~~[[12a]] 11b~~, and 11c. The transmission flip-flops 11a, ~~[[12a]] 11b~~, and 11c synchronize and output transmission data DATA_2A, DATA_1A, and DATA_0A, which are then transmitted to the signal transmission lines 30a, 30b and 30c.